

FACTA UNIVERSITATIS

Series: **Electronics and Energetics** Vol. 31, N° 1, March 2018, pp. 155 - 155

<https://doi.org/10.2298/FUEE1801155E>

## Retraction

**Mohammad Maghsoudloo, Hamid R. Zarandi**

**PARALLEL EXECUTION TRACING: AN ALTERNATIVE SOLUTION TO EXPLOIT UNDER-UTILIZED RESOURCES IN MULTI-CORE ARCHITECTURES FOR CONTROL-FLOW CHECKING.**

**Facta Universitatis, Series: Electronics and Energetics (FU Elec Energ), Vol. 29, No 2, June 2016, pp. 243 - 260. DOI: 10.2298/FUEE1602243M**

The article: PARALLEL EXECUTION TRACING: AN ALTERNATIVE SOLUTION TO EXPLOIT UNDER-UTILIZED RESOURCES IN MULTI-CORE ARCHITECTURES FOR CONTROL-FLOW CHECKING. Mohammad Maghsoudloo, Hamid R. Zarandi. Facta Universitatis, Series: Electronics and Energetics, Vol. 29, No 2, June 2016, pp. 243-260, DOI: 10.2298/FUEE1602243M, repeats 62% data already published in: An efficient adaptive software-implemented technique to detect control-flow errors in multi-core architectures. Mohammad Maghsoudloo, Hamid R. Zarandi, Navid Khoshavi. Microelectronics Reliability, Vol. 52, Issue 11, November 2012, pp. 2812-2828, DOI: [doi.org/10.1016/j.microrel.2012.03.033](https://doi.org/10.1016/j.microrel.2012.03.033) without any referencing.

Link to the retracted article – DOI: 10.2298/FUEE1602243M